EPI Announces SC25 Attendance to present results: Proactive Compute Showcases FAUST and FEVER

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The **European Processor Initiative** (EPI) https://www.european-processor-initiative.eu/, a project with 30 partners from 10 European countries, with the goal of achieving Europe's independence in HPC chip technologies and infrastructure, is nearing the finishing line, at the end of a two-stage run.

The project team will attend the Supercomputing 25, premier global event in the HPC and AI fields, to be held from November 16-21 in St. Louis, Missouri, USA. As a part of the EPI team at the booth **#4410**, **Proactive Compute**, a new spin-off from University of Zagreb's Faculty of Electrical Engineering and Computing (UNIZG-FER), will be ready to discuss and showcase two of its key IP's **FAUST** and **FEVER**.

FAUST is a **high-performance**, pipelined **floating-point unit** (FPU) IP designed for integration into RISC-V cores with vector processing capabilities. Some key advantages of FAUST are:

- fully compliant with the IEEE 754-2019 floating-point standard and RVV 1.0
- designed with a focus on minimizing its area while maintaining a rich feature set
- featuring a highly optimized pipeline
- configurable and flexible
- silicon-proven

Its advanced 2.0 version achieved significant area reduction while preserving a comprehensive feature set and full standards compliance. It has a configurable manual placement of pipeline registers to ensure an optimally segmented pipeline, eliminating the unpredictable and sub-optimal results from automatic retiming from synthesis tools.

FEVER, a UVM-based RISC-V FPU **verification environment** that complements FPU design. It is a SystemVerilog DPI wrapper which integrates FEVER into the UVM scoreboard to evaluate UUT's outputs.

"We are thrilled to present **FEVER** and **FAUST** at **SC25**, marking a significant step forward in our mission to advance high-performance computing," said **Dr. Mario Kovač**, Founder and CEO of **Proactive Compute**. "Designing and delivering advanced computational units to

power the next generation of **RISC-V** processors has been both a complex and inspiring journey. **FAUST**, now silicon-proven and validated through several strategic EU-funded projects, exemplifies our commitment to innovation and technical excellence — and is now available for licensing from **Proactive Compute**."

"The European Processor Initiative (EPI) proudly celebrates the launch of Proactive Compute, a company it helped bring to life. Through the FAUST and FEVER IPs now marketed by Proactive Compute, EPI continues to demonstrate not only its capacity for cutting-edge innovation but also its ability to translate technological breakthroughs into real-world impact. Together, EPI and Proactive Compute are reinforcing Europe's leadership in high-performance computing (HPC) and AI processor technologies—guided by European values and committed to advancing the continent's long-term strategic interests," explains Eric Monchalin, Chair of the EPI Board.

For more comprehensive details about FAUST and FEVER, please visit Proactive Compute website http://www.proactivecompute.com/.

About EPI

The European Processor Initiative (EPI) is a project whose aim is to design and implement a roadmap for a new family of low-power European processors for extreme scale computing, high-performance Big-Data and a range of emerging applications.

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