

Athens)

(Daniele Rossi – UNIPI)

(position statements &

moderated discussion)

TOPIC	SPEAKER
Welcome	(FORTH)
Welcome & Agenda outline	(EPI-PMO)
Keynote: "Performance analysis of ML workloads"	(Jesus Labarta - BSC)
"Experiences and learnings with the EPAC test-chip"	(Filippo Mantovani - BSC)
<coffee 20'="" break:=""></coffee>	
"SiPearl: Overview and roadmap of Rhea processors"	(Yingchih Yang – SiPEARL) – TBC
"Semidynamics: Overview and roadmap of RISC-V IP cores"	(Jordi Cortina – SMD)
<lunch 60'="" break:=""></lunch>	
"Experiences and perspectives from the EUPEX project"	(Matteo Turisini - CINECA)
"Experiences and perspectives from the EUPILOT project"	(Carlos Puchol – BSC)
<coffee 20'="" break:=""></coffee>	
"Silent Data Corruptions: Demystifying the Unmeasurable"	(Dimitris Gizopoulos – U. Athens)



## **DAY2 - SEP 11**

TIME	TOPIC	SPEAKER
09:45	Welcome & Agenda outline	(EPI-PMO)
10:00	Keynote: "Chiplets and Interposers: Architectural Trends, Integration Technologies, and Strategic Opportunities for Europe"	(Denis Dutoit - CEA)
11:00	<coffee 20'="" break:=""></coffee>	
11:20	"Experiences from initiating an Arm HPC ecosystem in Europe"	(Jean-Pierre Panziera, Eviden & ETP4HPC)
11:55	"Perspective on Arm processors and GPU acceleration in HPC"	(Filippo Spiga, NVIDIA) - TBC
12:25	"Advancing European Sovereignty in HPC with RISC-V: Overview of the DARE project"	(Paul Carpenter - BSC)
13:00	<lunch 60'="" break:=""></lunch>	
14:00	"Perspective on the European ecosystem on HPC, Cloud, AI"	(Matteo Mascagni - EuroHPC JU)
14:35	"Experiences and perspective from European RISC-V chip projects"	(Lluc Alvarez – BSC & Zettascale Lab)
15:10	<coffee 20'="" break:=""></coffee>	
15:30	"Retrospective on the design and validation of the memory hierarchy in RISC-V prototypes"	(Vassilis Papaefstathiou - FORTH)
16:00	"The EPI Common Platform"	(presentation & discussion - TBC)
16:45	Panel: "Elevating the potential of the European ecosystem: Obstacles and Ways Forward"	(position statements & discussion)
17:45	Expected end of day-2 of open workshop.	



Social event

Unmeasurable"

"RAS Concerns for RISC-V Systems"

Panel: "European Chips: Technology

Expected end of day-1 of open workshop

Challenges & Opportunities"

TIME

09:30

09:50 10:00

11:00

11:40

12:00

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