

EPI FORUM AGENDA DRAFT

DAY 1 – March 16th, 2020

SECTION	FROM	TO	TOPIC
Intro and EPI	14:00	15:45	Introduction (Jean-Marc Denis, EPI)
			EC Keynote (Thomas Skordas, EC)
			EPI main keynote (Yingchih Yang, EPI): Why EPI, Why this architecture Raise of the EPI Common Platform as EU central computation unit Overview of processing cores (Arm, RISC-V, MPPA, eFPGA, Sec, ...)
			SiPearl (Philippe Notton, SiPearl)
WW technology section	15:45	18:30	WW views from technology providers (Brent Gorda, Arm)
			Coffee break
			WW views from technology providers (Atos)
			WW views from technology providers (Steve Scott, HPE)
			WW views from technology providers (Luc Elman, Synopsys)
			WW views from technology providers (Paul de Bot, TSMC Europe)
ARM in high-end HPC: first feedback from Japan, USA and Europe	18:30	20:00	ARM in HPC - experience from Japan, USA (Mitsuhsa Sato, RIKEN; Robert Hoekstra, Sandia National Laboratories)
			Round table: Bringing new EU HPC processor to market technology and application view, closed vs open source IP
Party time	20:00	23:00	Platinum Sponsor Dinner

DAY 2 – March 17th, 2020

SECTION	FROM	TO	TOPIC
RISC-V keynote	9:00	9:30	RISC-V: an HPC view – keynote (Calista Redmond, RISC-V)
eHPC Module	9:30	11:45	eHPC automotive view
			WW views from technology providers (Ravi Subramanian, Mentor, a Siemens business)
			WW views from HPC/AI resources provider (GENCI)
			Round table: eHPC
	11:45	14:00	Coffee break
			EPI Accelerator Cores (Jesus Labarta, EPI)
			Automotive HPC use case: Electric hypercars (Sacha Vrazic, Rimac Automobili)
			Edge HPC use case: Autonomous Shuttle (NavyaTech)
EPI 'what's next' module	14:00	15:30	Lunch
			EPI software stack and programming models (Romain Dolbeau, EPI)
			WDC, Cudasip (Zvonimir Bandic, Jerry Ardizzone)
			Round table: Exascale applications
	15:30	15:45	Forum Close