

# **EUROPEAN PROCESSOR INITIATIVE**

FRAMEWORK PARTNERSHIP AGREEMENT IN EUROPEAN LOW-POWER MICROPROCESSOR TECHNOLOGIES

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#### Jean-Marc Denis, EPI Chairman of the Board

European Processor Initiative will deliver key technologies to the new European HPC strategic plan for an independent and innovative European high-performance computing and data ecosystem. Energy efficient high-performance families of EPI processors will include most advanced general-purpose and accelerator cores that will deliver unprecedented processing capabilities, enabling EU researchers from academia and industry to most efficiently address global challenges. The business sustainability of the initiative is supported by carefully balanced target markets, with primary focus on exascale HPC/AI and automotive markets.

Source: https://www.european-processor-initiative.eu/dissemination-material/press-release-first-steps-towards-a-made-in-europe-high-performance-microprocessor/

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### Philippe Notton, General Manager of EPI

It is a privilege to lead this consortium and enable the creation of a new big player in the field of advanced semiconductors in Europe. We have the best teams, and a huge portfolio of expertise on board from deep node submicron, co-Design, computer science, to HPC, and automotive end-products. We expect to ship from 2021 our 1st high class and high-performance solution.

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#### **Professor Mateo Valero, Director of BSC**

Acceleration is crucial to continued performance gains while reducing power consumption in computing. In EPI, the first accelerator will begin from RISC-V technology to deliver two unique vector and artificial intelligence accelerators for HPC and AI, since future supercomputers will be mostly heterogeneous; the second accelerator, based on Kalray's IP, will lead the path to deterministic automotive computation. Both are offering a European solution to future global converged (HPC and AI) computing needs.

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## Philippe Notton, General Manager of EPI

The combination of general-purpose processors, hardware accelerators, security modules, and further IP modules on a system-on-chip is one of the key success factors for realizing a high-performant and energy-efficient automotive computing platform for autonomous driving and connected mobility.

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