

## First steps towards a made-in-Europe high-performance microprocessor July 17<sup>th</sup> 2019, Barcelona

(Co-located with the ACM 2019 Summer school on HPC architectures for AI and dedicated applications)

### AGENDA

| Start | Finish | Topic  | Presenter                                    |
|-------|--------|--|--|
| 14:00 | 14:25  | <b>HPC processor landscape</b>                 | Denis Dutoit, CEA                            |
|       |        | Main challenges for HPC processor              |  |
|       |        | Architecture evolution towards heterogeneity   |  |
|       |        | Semiconductor technology overview              |  |
|       |        | European landscape and introduction to EuroHPC |  |
| 14:25 |        | <b>European Processor Initiative (EPI)</b>     |  |
| 14:25 | 14:35  | Overview                                       | Denis Dutoit, CEA                            |
| 14:35 | 14:50  | Processor and general architecture             | Denis Dutoit, CEA                            |
| 14:50 | 15:00  | Co-design process and modeling                 | Denis Dutoit, CEA                            |
| 15:00 | 15:45  | Software                                       | Jesús Labarta, BSC                           |
| 15:45 | 16:30  | Accelerator                                    | Mauro Olivieri, BSC                          |
| 16:30 | 17:00  | Automotive                                     | Jaume Abella, BSC;<br>Francisco Cazorla, BSC |
|       | 17:00  | <b>End</b>                                     |  |

### LOCATION

Sala Àgora room, Plaça Telecos, North Campus UPC.

Map: <https://goo.gl/maps/kGAgqhrPwkpd9jQt9>

### REGISTRATION

Registration is mandatory, please fill out the following form:

<https://katarinavukusic.typeform.com/to/H5eERC>

Information you provide is necessary only for organizational purposes, it will not be forwarded to any third party. Registration closes on **July 8<sup>th</sup> or sooner if the tutorial is at capacity.**