

European Processor Initiative



European Processor Initiative

P. Notton / GM European Processor Initiative SIA CEA 5.0, Dec 5th 2018

philippe.notton@european-processor-initiative.eu



- 1. Genesis
- 2. Setup
- 3. Benefits of scalability
- 3. Roadmap
- 4. Challenges
- 5. Conclusions



From Supercomputers to Autonomous cars

(source = Gartner, July 2018)

	Class			
Compute, Data & Communication	3	4	5	
Processing Capacity (TFLOPS)	40	80	120	
DRAM (GB)	1	10	20	
Non Volatile Storage GB)	100	300	500	
data link interface (Gbps)	0,1	1	1	

Only 10 Class 5 Vehicles in 2024 are equivalent to a current Top500 Supercomputer

compute power consumption (watts to reach processing power capacity)	3	4	5	
35 Gflops/w (GPP exascale class proc >2023)	1 143 W	2 286 W	3 429 W	General Purpose (very) high-end processors in 2024
What if 70 Gflops/w	571 W	1 143 W	1 714 W	Accelerator high-end processors in 2024
What if 140 Gflops/w	286 W	571 W	857 W	Accelerator (Most) high-end processors in 2024
What if 280 Gflops/w	143 W	286 W	429 W	Likely unrealistic in 2024



1. Genesis

- 2. Setup
- 3. Benefits of scalability
- 3. Roadmap
- 4. Challenges
- 5. Conclusions

NSA May Have Backdoors Built Into Intel And **AMD Processors**

Amazon exec and Super Micro CEO call retraction of sny chip of

The US Cloud Act v The EU's GDPR - Data Privacy &

Security

of researchers showed how a Tesla l S can be hacked and stolen in second using \$600 worth of equipment



Car hacking remains a very real threat as autos become ever more loaded with tech

A jet sale to Egypt is being blocked by a US regulation, and France is over it







HPC for Researchers and scientists

(resources and time)



Underpinning innovation in almost all scientific disciplines Deeper insights into unexplored systems of high complexity





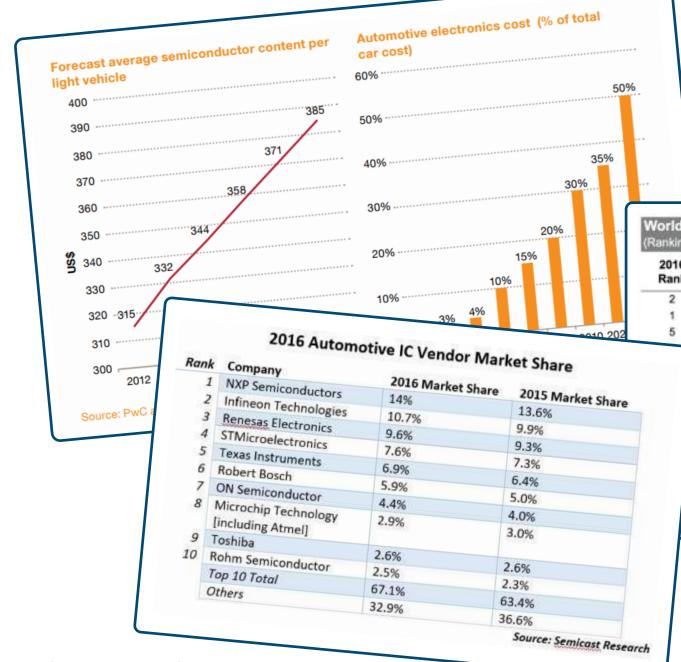
Compared to its competitors in the USA, China and Japan, Europe is underinvesting in supercomputing, with an annual funding gap of €500-750 million.2

But...



In June 2012, the EU had 4 machines in the global top 10 supercomputers. Today the fastest system in the EU ranks 13 on the global list - about 10 times slower than the world's fastest machine.1





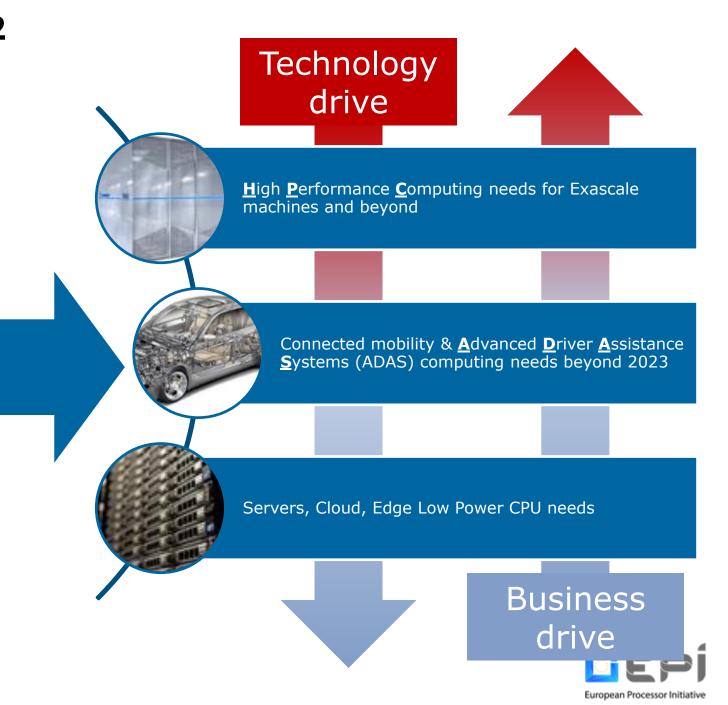
2016 Rank	2017 Rank	Company Name	2016 Revenue(\$)	2017 Revenue(\$)	Revenue Percent Change	Revenue Percent of Total	Revenue Cumulative Percent
2	1	Samsung Electronics	40,389	62,031	53.6%	14.5%	14.5%
1	2	Intel	54,980	61,406	11.7%	14.3%	28.8%
5	3	SK Hynix	14,699	26,638	81.2%	6.2%	35.0%
	4	Micron Technology	12,710	22,843	79.7%	5.3%	40.3%
	5	Broadcom Limited	14,979	17,375	16.0%	4.0%	44.3%
	6	Qualcomm	15,405	16,872	9.5%	3.9%	48.3%
	7	Texas Instruments	12,836	14,525	13.2%	3.4%	51.7%
	8	Toshiba	9,904	11,864	19.8%	2.8%	54.4%
	9	NXP	9,306	8,864	-4.7%	2.1%	56.5%
	10	nVidia	6,030	8,578	42.3%	2.0%	58.5%
		Top 10 Companies	191,238	250,996	31.2%	58.5%	
		All Others	161,356	178,112	10.4%	41.5%	
		Total Semiconductor	352,594	429,108	21.7%	100.0%	

EC expectations from ICT-42 & EPI value proposal

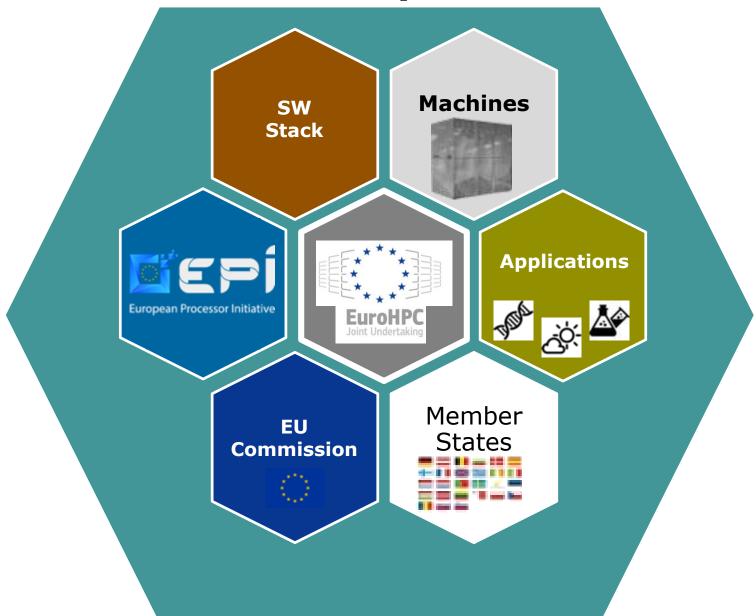
EPI expected impacts (as per EC request)

- Get a world class processor for the Exascale machines supplied by EuroHPC in 2023
- Develop a sustainable economic model

EPI is an H2020 project but with industrial mindset & product delivery oriented



EPI is an essential part of EuroHPC



- European High Performance Computing Joint Undertaking (EuroHPC JU):
 - Setup in Nov 2018 (operational until 2026)
 - Composed of public and private members
 - Budget of ~1B€ (50% EU, 50% participating countries)+400M€ from private entities
 - Will provide financial support
 - (public procurement, Research and Innov. Grants)





1. Genesis

2. Setup

- 3. Benefits of scalability
- 3. Roadmap
- 4. Challenges
- 5. Conclusions

23 partners: Wide expertise and excellent combo















AUTOMOTIVE FOCUS































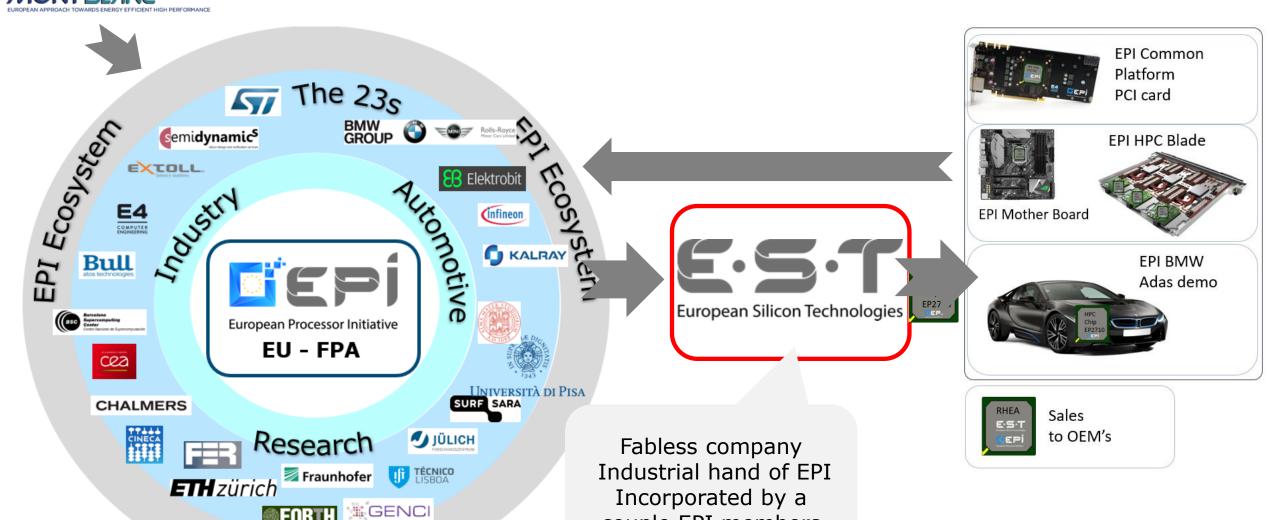








EPI 23 partners, from research to industry from consortium to EU high-tech fabless

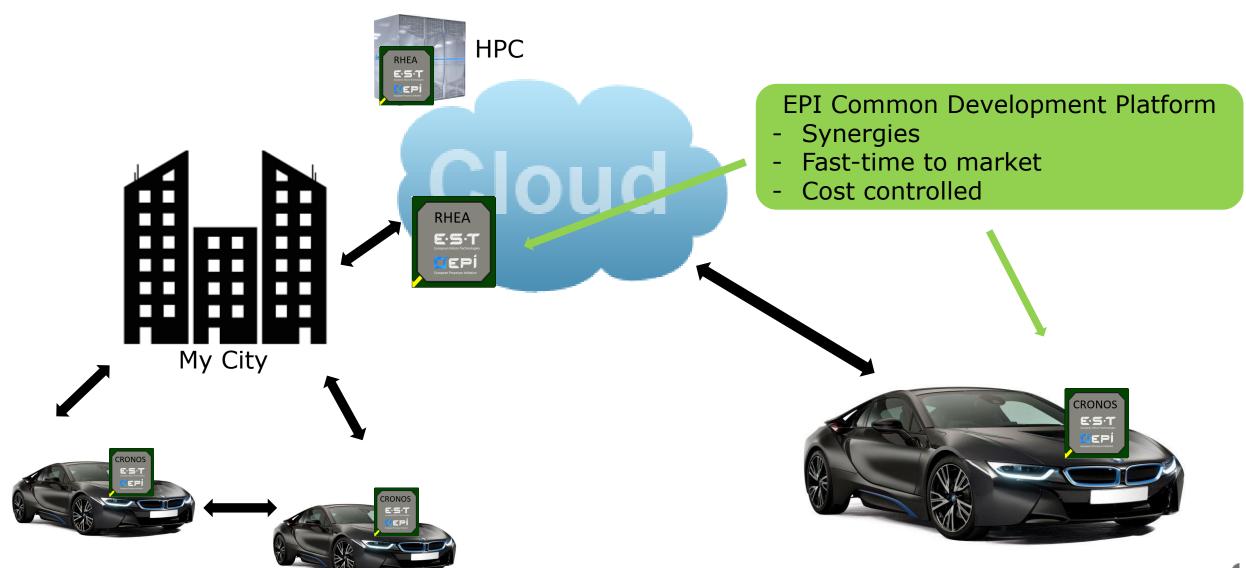


couple EPI members

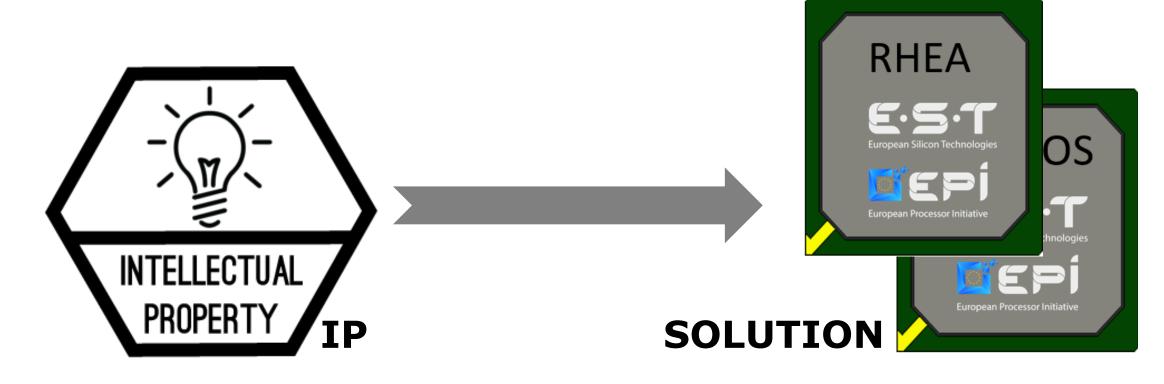


EPI Ecosystem

EPI: End2End Solutions for both Servers and Edge



Unique in the universe of H2020 programs: From IP to Product/Solution









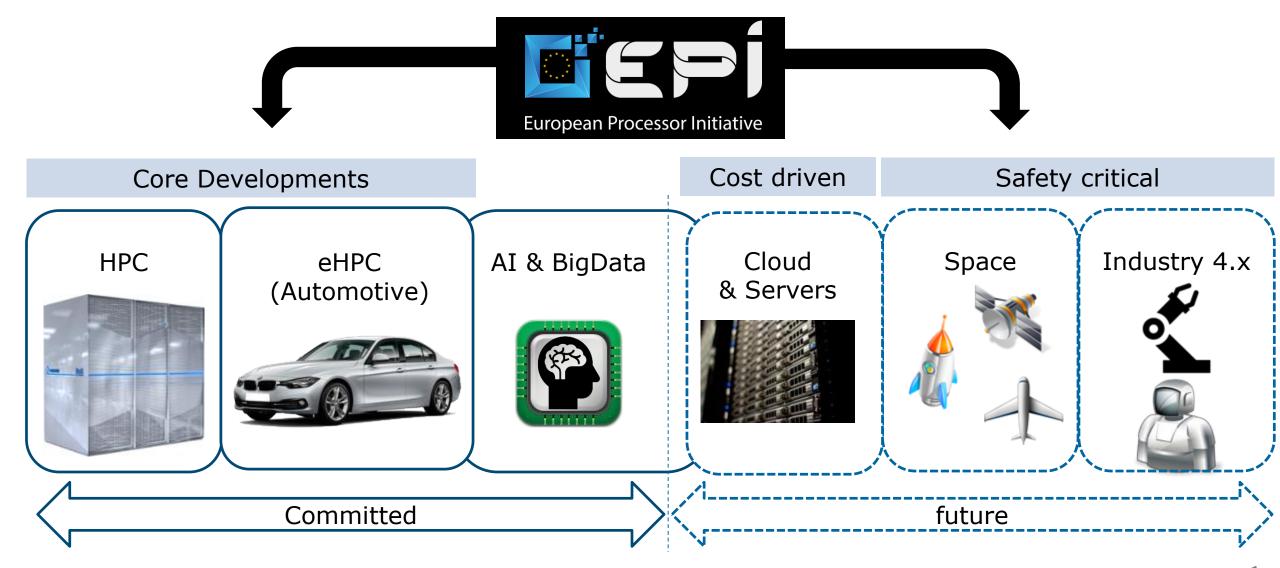


- 1. Genesis
- 2. Setup

3. Benefits of scalability

- 3. Roadmap
- 4. Challenges
- 5. Conclusions

Scalability allows wide market potential coverage



EPI technologies will be everywhere to protect you or your business

To control your autonomous car safely





To help National and European sovereignty, science and research

To protect your digital life and your personal data's







- 1. Genesis
- 2. Setup
- 3. Benefits of scalability

3. Roadmap

- 4. Challenges
- 5. Conclusions

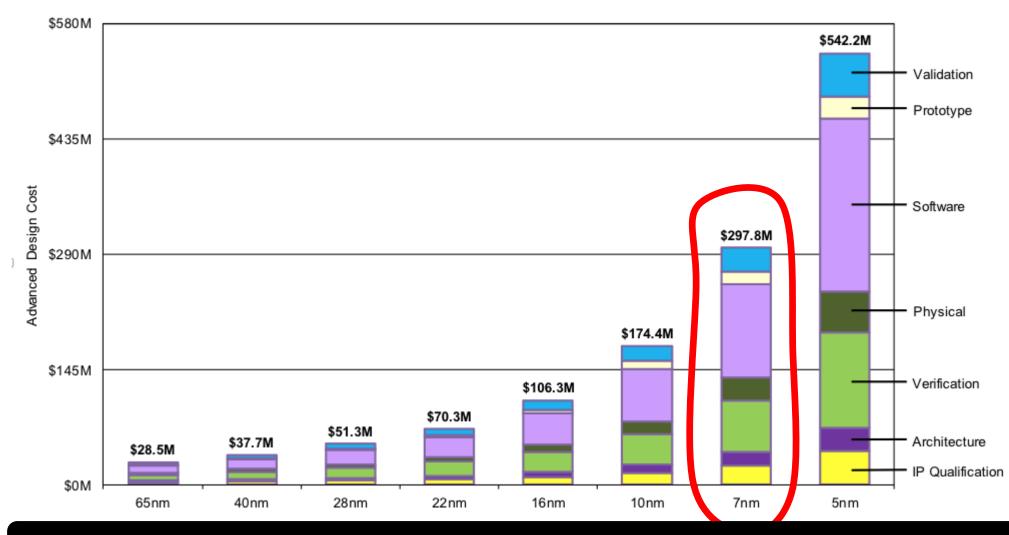
EPI Common Platform HPC System Exascale HPC System PreExascale Automotive CRONOS **CPU** EST Adv 64bits core **RHEA** & **₹** RISC-∨ ESIT Adv. 64bits core Gen 1 GPP & **!** RISC-V Gen 2 GPP **Many IPs Some IPs Few IPs Integration EPI Common Platform** EPI IP's launch pad **Pan European Research Platform for HPC & AI External** 2021 2023 2024 2022 **IPs**

European Processor Initiative



- 1. Genesis
- 2. Setup
- 3. Benefits of scalability
- 3. Roadmap
- 4. Challenges
- 5. Conclusions

Challenge 1: Cost of Advanced Design SoC



9 digits budget....Building such a Product needs lots of cash...



While EPI is warming up, out there....

E Reaps \$58M To Speed Development of Its 7nm Al Chip Set to Raise \$100 ິລັ ມy intel Capital

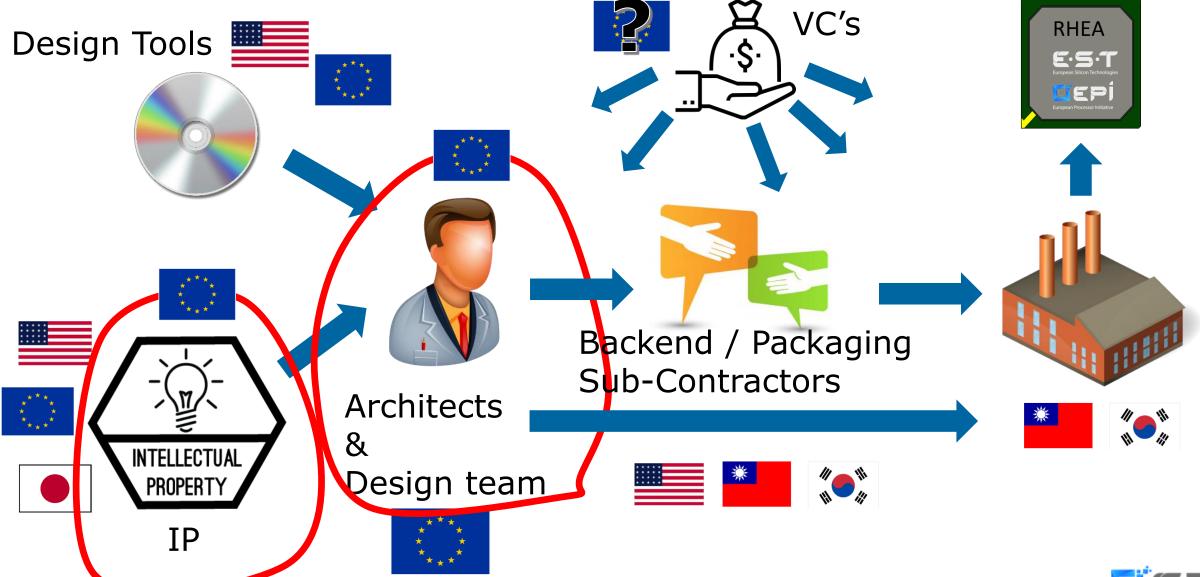
Raises US\$100 resident launches new Million In Series A Funding , with backing from Jariyle Group

Intel Leads \$75 Million Investment in AI Chip Startup H

A stealthy startup called C \$25 million to build deep learning hardware \$50M Series C raised around



Challenge 2: What is really European?





- 1. Genesis
- 2. Setup
- 3. Benefits of scalability
- 3. Roadmap
- 4. Challenges

5. Conclusions

EPI Moon Shot



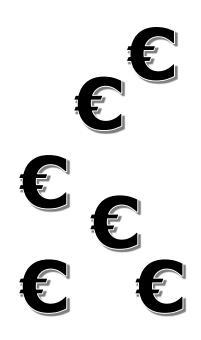


Airbus (1969) → 2003 to take over Boeing

With EPI, Europe has the ambition to repeat the Airbus success



To anticipate our 2019 Financial Roadshow







European Union Automotive Embedded Computing





THANK YOU



European Processor Initiative

contact@european-processor-initiative.eu